

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/758,527	01/16/2004	Tsuneo Abe	03288-1/2003-011440	5599	
21254	7590 03/25/2005		EXAM	EXAMINER	
MCGINN & GIBB, PLLC 8321 OLD COURTHOUSE ROAD			CHARIOUI, MOHAMED		
SUITE 200		ART UNIT	PAPER NUMBER		
VIENNA, V	A 22182-3817		2857		
			DATE MAILED: 03/25/2009	ς.	

Please find below and/or attached an Office communication concerning this application or proceeding.

U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04)

1) Notice of References Cited (PTO-892)

Paper No(s)/Mail Date 3/25/04.

Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)

4) Interview Summary (PTO-413)

6) Other:

Paper No(s)/Mail Date. ___

5) Notice of Informal Patent Application (PTO-152)

Application/Control Number: 10/758,527

Art Unit: 2857

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1-6 are rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential steps, such omission amounting to a gap between the steps. See MPEP § 2172.01. The omitted step is: how the semiconductor device is being tested.

Claims 1-6 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The limitation "controlling said output circuits to turn ON said first and second transistors of a first output circuit among said plurality of output circuits and turn ON and OFF said first transistor and said second transistor, respectively, a second output circuit among said plurality of output circuits". It is unclear whether the first and second transistors that are to be turned ON and OFF, respectively, belong to the second circuit or the first circuit. Therefore claims 1-6 are considered to be indefinite.

Claim 6 recites "A recording medium for storing therein a program for running a computer thereon to conduct the method according to claim 1" in page 21, lines 14-15. Claim 6 is considered vague and indefinite because it limits the method in parent claim 1 with a specific apparatus, thereby incorporating both product and method subject matter.

Application/Control Number: 10/758,527

Art Unit: 2857

Claim Rejections - 35 USC § 101

2. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the

Page 3

conditions and requirements of this title.

Claim 6 is rejected under 35 U.S.C. 101 because the claimed invention is

directed to non-statutory subject matter.

Claim 6 is rejected under 35 U.S.C. 101 since the claim is directed to neither a

"process" nor a "machine" but rather embraces or overlaps two different statutory

classes of inventions set forth in 35 U.S.C. 101 which is drafted so as to set forth the

statutory classes of invention in the alternative only.

Allowable Subject Matter

3. Claims 7-14 are allowed.

The following is a statement of reasons for the indication of allowable subject

matter: None of the prior art of record teaches or suggests a control circuit for

controlling said output circuits during a test mode to turn ON said first and second

transistors of said first output circuit, and turn ON and OFF said first transistor and said

second transistor, respectively, of said second output circuit, in combination with the

rest of the claim limitations.

Prior art

4. The prior art made record and not relied upon is considered pertinent to

applicant's disclosure:

Dedic ['830] discloses differential switching circuitry.

Bhagwat et al. ['103] disclose full range soft-switching DC-DC converter.

ند ، ني

Application/Control Number: 10/758,527

Art Unit: 2857

Maloberti et al. ['525] disclose low switching noise output buffer.

Tanzawa et al. ['606] disclose semiconductor memory device and high-voltage

Page 4

switching circuit.

Contact information

5. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Mohamed Charioui whose telephone number is (571)

272-2213. The examiner can normally be reached Monday through Friday, from 9 am

to 6 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Marc S Hoff can be reached on (571) 272-2216. The fax phone number for

the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Mohamed Charioui

3/18/05

TECHNOLOGY CENTER 2800